## **EAST Search History**

| Ref# | Hits | Search Query   | DBs   | Default<br>Operator | Plurals | Time Stamp          |
|------|------|--|---|---------------------|---------|---------------------|
| L1   | 2    | "7237332".pn.  | US-PGPUB;<br>USPAT; USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR                  | ON      | 2009/03/16<br>07:59 |
| L2   | 2    | "5049221".pn.  | US-PGPUB;<br>USPAT; USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR                  | ON      | 2009/03/16<br>08:11 |
| L3   | О    | (support or wafer or carrier or substrate or base) same conductor and (insulat\$4 or oxide or dielectric) same ((copper or "Cu") near foil) and (press\$4 same ((copper or "Cu") near foil)) same (insulat\$4 or oxide or dielectric) and pressuriz\$4 and (b near stage) and (plating or electroplating) same conductor and 438/624.ccls. | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | MOR                 | ON      | 2009/03/16<br>08:22 |
| L4   | 7    | 438/624.ccls. and (b near stage)   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2009/03/16<br>08:22 |
| L5   | 7    | 438/624.ccls. and (b<br>near stage) and<br>(substrate or wafer or<br>carrier or base or<br>plate)  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2009/03/16<br>08:23 |
| L6   | 1    | 438/624.ccls. and (b<br>near stage) and<br>(substrate or wafer or<br>carrier or base or<br>plate) and thermo   | US-PCPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2009/03/16<br>08:35 |

| L7         | 2      | "5480048".pn.  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2009/03/16<br>08:38 |
|------------|--------|--|---|----|-----|---------------------|
| S1         | 0      | (("JP2001-537286")<br>or ("JP2001-68850")<br>or ("JP7-111375") or<br>("JP6-350258") or<br>("JP10-022636") or<br>("JP2002-137328") or<br>("JP2002-134881") or<br>("JP2002-076578")).<br>PN. | US-PGPUB;<br>USPAT; EPO;<br>JPO; DERWENT                                | OR | OFF | 2006/06/26<br>09:27 |
| <b>S</b> 2 | 37     | "5600103"  | US-PGPUB;<br>USPAT; USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR | ON  | 2006/06/26<br>10:34 |
| S3         | 172    | "power supply film"  | US-PGPUB;<br>USPAT; USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR | ON  | 2006/06/26<br>10:34 |
| <b>S</b> 4 | 1      | "power supply film"<br>and insulat\$4 and<br>conduct\$4 and "b-<br>stage"  | US-PGPUB;<br>USPAT; USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR | ON  | 2006/06/26<br>10:36 |
| <b>S</b> 5 | 4      | "power supply film"<br>and insulat\$4 and<br>conduct\$4 and<br>thermoset\$4  | US-PGPUB;<br>USPAT; USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR | ON  | 2006/06/26<br>10:36 |
| <b>S</b> 6 | 3      | "power supply film"<br>and insulat\$4 and<br>conduct\$4 and<br>thermoset\$4 and<br>curing  | US-PGPUB;<br>USPAT; USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR | ON  | 2006/06/26<br>10:37 |
| <b>S</b> 7 | 4      | "power supply film"<br>and insulat\$4 and<br>conduct\$4 and<br>thermoset\$4 and cur<br>\$3   | US-PGPUB;<br>USPAT; USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB          | OR | ON  | 2006/06/26<br>10:37 |
| S8         | 623590 | (conduct\$4 near9<br>(semiconductor or<br>wafer or substrate or<br>carrier or base))   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2007/04/02<br>08:43 |

| <b>S</b> 9 | 239119 | (conduct\$4 near9<br>(semiconductor or<br>wafer or substrate or<br>carrier or base)) and<br>((insulat\$4 or oxide<br>or dielectric) near9<br>conduct\$4)   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2007/04/02<br>08:44 |
|------------|--------|--|---|------|----|---------------------|
| S10        | 16737  | (conduct\$4 near9<br>(semiconductor or<br>wafer or substrate or<br>carrier or base)) and<br>((insulat\$4 or oxide<br>or dielectric) near9<br>conduct\$4) and (press<br>\$3 near9 (oxide or<br>dielectric insulat\$4))  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2007/04/02<br>08:47 |
| S11        | 381    | (conduct\$4 near9<br>(semiconductor or<br>wafer or substrate or<br>carrier or base)) and<br>((insulat\$4 or oxide<br>or dielectric) near9<br>conduct\$4) and (press<br>\$3 near9 (oxide or<br>dielectric insulat\$4))<br>and ( b near stage)                     | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR   | ON | 2007/04/02<br>08:48 |
| S12        | 52     | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (press \$3 near9 (oxide or dielectric insulat\$4)) and ( b near stage) and (power near supply)                        | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | COOR | ON | 2007/04/02<br>09:45 |
| S13        | 27     | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (press \$3 near9 (oxide or dielectric insulat\$4)) and ( b near stage) and (power near supply) and (support or frame) | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | MOR  | ON | 2007/04/02<br>09:50 |

| S14  | 11   | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (press \$3 near9 (oxide or dielectric insulat\$4)) and ( b near stage) and (power near supply) and (support or frame) and (plating near9 conduct\$4) | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR  | ON | 2007/04/02<br>10:12 |
|------|------|---|---|-----|----|---------------------|
| \$15 | 3    | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (pressing near9 (oxide or dielectric insulat\$4)) and ( b near stage) and (power near supply) and (support or frame) and (plating near9 conduct\$4)  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | MOR | ON | 2007/04/02<br>10:13 |
| S16  | 1982 | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (pressing near9 (oxide or dielectric insulat\$4))  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | MOR | ON | 2007/04/02<br>10:14 |
| S17  | 937  | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (pressing near9 (oxide or dielectric insulat\$4)) and (support or frame)   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | MOR | ON | 2007/04/02<br>10:14 |

| S18 | 86     | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (pressing near9 (oxide or dielectric insulat\$4)) and (support or frame) and (b near stage)   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB |        | ON | 2007/04/02<br>10:15 |
|-----|--------|--|---|--------|----|---------------------|
| S19 | 81     | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (pressing near9 (oxide or dielectric insulat\$4)) and (support or frame) and (b near stage) and ((heat\$4 or thermal) near9 (oxide or dielectric or insulat\$4))                          | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | · MOOR | ON | 2007/09/06<br>09:57 |
| S20 | 3      | (conduct\$4 near9 (semiconductor or wafer or substrate or carrier or base)) and ((insulat\$4 or oxide or dielectric) near9 conduct\$4) and (pressing near9 (oxide or dielectric insulat\$4)) and (support or frame) and (b near stage) and ((heat\$4 or thermal) near9 (oxide or dielectric or insulat \$4)) and (power near supply) | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | NOR    | ON | 2007/04/02<br>10:21 |
| S21 | 128560 | (conduct\$4 near9<br>(plating or etch\$4))   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR     | ON | 2007/09/24<br>14:37 |

| S22 | 111747 | (conduct\$4 near9<br>(plating or etch\$4))<br>and (support or base<br>pr carrier or arm or<br>substrate or wafer)   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/24<br>14:38 |
|-----|--------|---|---|----|----|---------------------|
| S23 | 35038  | (conduct\$4 near9 (plating or etch\$4)) and (support or base pr carrier or arm or substrate or wafer) and press\$4 and (insulat\$4 or dielectric or oxide) near9 (support or base pr carrier or arm or substrate or wafer)  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/24<br>14:40 |
| S24 | 7011   | (conduct\$4 near9 (plating or etch\$4)) and (support or base pr carrier or arm or substrate or wafer) and press\$4 and (insulat\$4 or dielectric or oxide) near9 (support or base pr carrier or arm or substrate or wafer) and (press\$4 near9 (insulat\$4 or dielectric or oxide)) | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/24<br>14:41 |
| S25 | 7010   | (conduct\$4 near9 (plating or etch\$4)) and (support or base or carrier or arm or substrate or wafer) and press\$4 and (insulat\$4 or dielectric or oxide) near9 (support or base or carrier or arm or substrate or wafer) and (press\$4 near9 (insulat\$4 or dielectric or oxide)) | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/24<br>14:42 |

| <b>S</b> 26 | 338   | (conduct\$4 near9 (plating or etch\$4)) and (support or base or carrier or arm or substrate or wafer) and press\$4 and (insulat\$4 or dielectric or oxide) near9 (support or base or carrier or arm or substrate or wafer) and (press\$4 near9 (insulat\$4 or dielectric or oxide)) and (electronic near part)                    | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/24<br>14:44 |
|-------------|-------|---|---|----|----|---------------------|
| <b>S</b> 27 | 46    | (conduct\$4 near9 (plating or etch\$4)) and (support or base or carrier or arm or substrate or wafer) and press\$4 and (insulat\$4 or dielectric or oxide) near9 (support or base or carrier or arm or substrate or wafer) and (press\$4 near9 (insulat\$4 or dielectric or oxide)) and (electronic near part) and (B near stage) | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/24<br>15:01 |
| S28         | 88010 | (conduct\$4 near portion)   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/11/26<br>09:55 |
| S29         | 12774 | (conduct\$4 near<br>portion) near9<br>(support or base or<br>substrate or wafer)  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/11/26<br>10:52 |
| S30         | 1242  | (conduct\$4 near portion) near9 (support or base or substrate or wafer) and (copper near9 (insulat\$4 or oxide or dielectric))  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/11/26<br>10:54 |

| S31 | 55 | (conduct\$4 near portion) near9 (support or base or substrate or wafer) and (copper near9 (insulat\$4 or oxide or dielectric)) and (b near stage)  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/11/26<br>10:54 |
|-----|----|--|---|----|----|---------------------|
| S32 | 46 | (conduct\$4 near portion) near9 (support or base or substrate or wafer) and (copper near9 (insulat\$4 or oxide or dielectric)) and (b near stage) and press \$4  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/11/26<br>10:55 |
| S33 | 27 | (conduct\$4 near portion) near9 (support or base or substrate or wafer) and (copper near9 (insulat\$4 or oxide or dielectric)) and (b near stage) and press \$4 and thermoplastic  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/11/26<br>10:56 |
| S34 | 25 | (conduct\$4 near portion) near9 (support or base or substrate or wafer) and (copper near9 (insulat\$4 or oxide or dielectric)) and (b near stage) and press \$4 and thermoplastic and (heat or anneal)                         | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/11/26<br>10:56 |
| S35 | 9  | (conduct\$4 near portion) near9 (support or base or substrate or wafer) and (copper near9 (insulat\$4 or oxide or dielectric)) and (b near stage) and press \$4 and thermoplastic and (heat or anneal) and (power near supply) | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/11/26<br>10:56 |

| S36         | 7      | ("5480048"   "5600103"   "5970319"   "6930388").PN. and (conduct\$4 or metal) with (substrate or wafer or carrier or base or plate or support)   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/07/07<br>09:30 |
|-------------|--------|--|---|----|----|---------------------|
| \$37        | 2      | ("5480048"   "5600103"   "5970319"   "6930388").PN. and (conduct\$4 or metal) with (substrate or wafer or carrier or base or plate or support) and (insulat \$4 or oxide or dielectric) and press \$4 with (insulat\$4 or oxide or dielectric) | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/07/07<br>09:32 |
| S38         | 380505 | (support or wafer or carrier or substrate or base) same conductor  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/03/06<br>09:52 |
| <b>S</b> 39 | 4336   | (support or wafer or carrier or substrate or base) same conductor and (insulat\$4 or oxide or dielectric) same ((copper or "Cu") near foil)  | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/03/06<br>09:53 |
| S40         | 1442   | (support or wafer or carrier or substrate or base) same conductor and (insulat\$4 or oxide or dielectric) same ((copper or "Cu") near foil) and (press\$4 same ((copper or "Cu") near foil))   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/03/06<br>09:54 |

| S41 | 1137 | (support or wafer or carrier or substrate or base) same conductor and (insulat\$4 or oxide or dielectric) same ((copper or "Cu") near foil) and (press\$4 same ((copper or "Cu") near foil)) same (insulat\$4 or oxide or dielectric)   | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/03/06<br>09:56 |
|-----|------|---|---|----|----|---------------------|
| S42 | 118  | (support or wafer or carrier or substrate or base) same conductor and (insulat\$4 or oxide or dielectric) same ((copper or "Cu") near foil) and (press\$4 same ((copper or "Cu") near foil)) same (insulat\$4 or oxide or dielectric) and pressuriz\$4                                    | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/03/06<br>10:04 |
| S43 | 56   | (support or wafer or carrier or substrate or base) same conductor and (insulat\$4 or oxide or dielectric) same ((copper or "Cu") near foil) and (press\$4 same ((copper or "Cu") near foil)) same (insulat\$4 or oxide or dielectric) and pressuriz\$4 and (b near stage)                 | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/03/06<br>10:04 |
| S44 | 49   | (support or wafer or carrier or substrate or base) same conductor and (insulat\$4 or oxide or dielectric) same ((copper or "Cu") near foil) and (press\$4 same ((copper or "Cu") near foil)) same (insulat\$4 or oxide or dielectric) and pressuriz\$4 and (b near stage) and (plating or | US-PGPUB;<br>USPAT; USOCR;<br>FPRS; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/03/06<br>10:05 |

|             |    | electroplating)  |               | *************************************** | l<br>l |                     |
|-------------|----|--|---------------|---|--------|---------------------|
| <b>S</b> 45 | 23 | (support or wafer or carrier or substrate or base) same conductor and (insulat\$4 or oxide or dielectric) same ((copper or "Cu") near foil) and (press\$4 same ((copper or "Cu") near foil)) same (insulat\$4 or oxide or dielectric) and pressuriz\$4 and (b near stage) and (plating or electroplating) same conductor | USPAT; USOCR; | MOR                                     | ON     | 2009/03/06<br>10:05 |

## 3/16/09 9:04:47 AM

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